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Substitute for form 1449A/PFO				Complete if Known	
<b>INFORMATION DISCLOSURE STATEMENT BY APPLICANT</b>				Application Number	09/843,486
				Filing Date	April 25, 2001
				First Named Inventor	Arash Hassibi
				Art Unit	2811
				Examiner Name	Quang D. Vu
Sheet	1	of	1	Attorney Docket Number	4363P004

NON PATENT LITERATURE DOCUMENTS				
Examiner Initials*	Cite No.†	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.		1‡
qv		JERSTENSON M DELM ET AL: "Optimal Design Of A CMOS OP-AMP Via Geometric Programming", IEEE Transactions On Computer-Aided Design Of Integrated Circuits and Systems, January 2001, IEEE Vol. 20, No. 1, Pages 1-21, Paragraph '007A', Table 1.		
qv		DALASA, F: "Modeling Non-Slicing Floorplans With Binary Trees", 2000 IEEE/ACM International Conference On Computer-Aided Design. ICCAD-2000, San Jose, CA, Nov. 5-9, 2000, IEEE/ACM International Conference On Computer-Aided Design, New York, NY, IEEE, Nov. 5, 2000, Pages 13-16.		

Examiner Signature:		Date Considered:	05/24/04
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\*Examiner: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication.

†Applicant's unique citation designation number. ‡Applicant is to place a check mark here if English language translation is attached.

Based on PTO/EHDP (00-03) as modified by Blakey, Skoloff, Taylor & Zafman (w/1) 08/11/2003  
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